



Vishay Semiconductors

Schottky Rectifier, 1.0 A





| PRODUCT SUMMARY | | | | |
|----------------------------------|----------------|--|--|--|
| Package | DO-214AA (SMB) | | | |
| I _{F(AV)} | 1 A | | | |
| V _R | 90 V, 100 V | | | |
| V _F at I _F | 0.78 V | | | |
| I _{RM} | 1 mA at 125 °C | | | |
| T _J max. | 175 °C | | | |
| Diode variation | Single die | | | |
| E _{AS} | 1.0 mJ | | | |

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-MBRS190TRPbF, VS-MBRS1100TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | |
|-----------------------------------|----------------------------------|-------------|-------|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | |
| I _{F(AV)} | Rectangular waveform | 1.0 | Α | | |
| V _{RRM} | | 90/100 | V | | |
| I _{FSM} | t _p = 5 μs sine | 870 | Α | | |
| V _F | 1.0 Apk, T _J = 125 °C | 0.63 | V | | |
| T _J | Range | - 55 to 175 | °C | | |

| VOLTAGE RATINGS | | | | | |
|--------------------------------------|-----------|-----------------|------------------|-------|--|
| PARAMETER | SYMBOL | VS-MBRS190TRPbF | VS-MBRS1100TRPbF | UNITS | |
| Maximum DC reverse voltage | V_{R} | 90 | 100 | V | |
| Maximum working peak reverse voltage | V_{RWM} | 90 | | | |

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|---|--------------------|---|--|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum average forward current | I _{F(AV)} | 50 % duty cycle at T _L = 147 °C, rectangular waveform | | 1.0 | |
| Maximum peak one cycle non-repetitive surge current | 1 | 5 μs sine or 3 μs rect. pulse | Following any rated load condition and with rated V _{RRM} applied | 870 | Α |
| | IFSM | 10 ms sine or 6 ms rect. pulse | | 50 | |
| Non-repetitive avalanche energy | E _{AS} | T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH | | 1.0 | mJ |
| Repetitive avalanche current | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 0.5 | | А | |

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| ELECTRICAL SPECIFICATIONS | | | | | | |
|---------------------------------|--------------------------------|---|---------------------------------------|--------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS | |
| Maximum forward voltage drop | V _{FM} ⁽¹⁾ | V _{EM} ⁽¹⁾ 1 A | T _J = 25 °C | 0.78 | V | |
| See fig. 1 | VFM (*) | T _J | T _J = 125 °C | 0.62 | V | |
| Maximum reverse leakage current | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.5 | - mA | |
| See fig. 2 | 'RM ''' | T _J = 125 °C | | 1.0 | | |
| Typical junction capacitance | C _T | $V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C | | 42 | pF | |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body | | 2.0 | nH | |
| Maximum voltage rate of change | dV/dt | Rated V _R | | 10 000 | V/µs | |

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | |
|---|--|--------------------------------------|-------------|---------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS | |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | - 55 to 175 | °C | |
| Maximum thermal resistance, junction to lead | R _{thJL} ⁽²⁾ | DC operation See fig. 4 | 36 | °C AM | |
| Maximum thermal resistance, junction to ambient | R _{thJA} | DC operation | 80 | °C/W | |
| Approximate weight | | | 0.10 | g | |
| Approximate weight | | | 0.003 | OZ. | |
| Marking device | | Case style SMB (similar to DO-214AA) | V19/ | V19/V10 | |

Notes

 $[\]frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB



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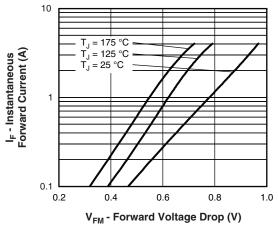


Fig. 1 - Maximum Forward Voltage Drop Characteristics

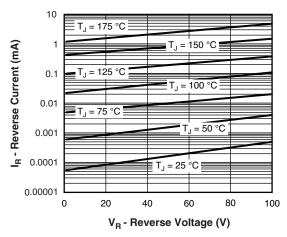


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

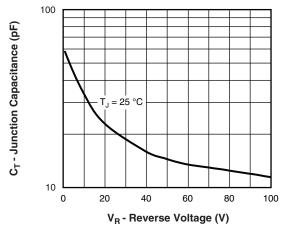


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

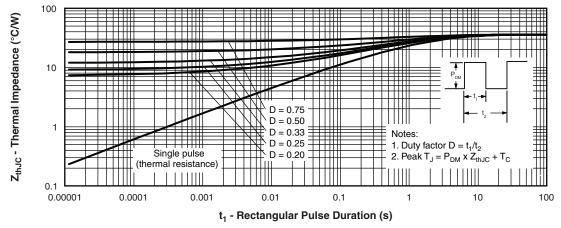


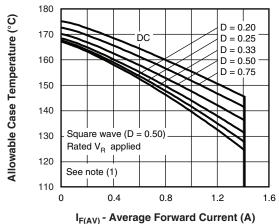
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

VS-MBRS190TRPbF, VS-MBRS1100TRPbF

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Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

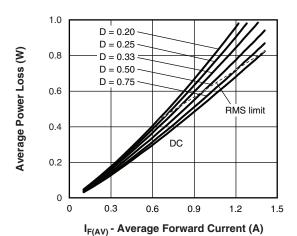


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

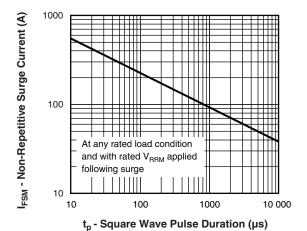


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R



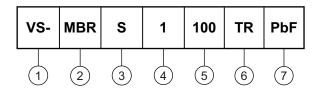
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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product suffix

2 - Schottky MBR series

3 - S = SMB

Current rating (1 = 1 A)

90 = 90 V 100 = 100 V

6 - TR = Tape and reel (3000 pieces)

7 - PbF = Lead (Pb)-free

| LINKS TO RELATED DOCUMENTS | | | | |
|----------------------------|--------------------------|--|--|--|
| Dimensions | www.vishay.com/doc?95017 | | | |
| Part marking information | www.vishay.com/doc?95029 | | | |
| Packaging information | www.vishay.com/doc?95034 | | | |



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